

### REMARKS

Claims 1-4, 21 and 29-32 are pending. The Examiner's reconsideration of the rejections is respectfully requested in view of the amendments and remarks.

Applicants appreciate the Examiner's indication that claim 21 is allowed.

Claims 1 and 2 have been rejected under 35 U.S.C. 103(a) as being unpatentable over U.S. Patent No. 5,606,343 to Tsuboyama et al. in view of U.S. Patent No. 6,380,942 to Hussain et al. and further in view of U.S. Patent No. 5,247,363 to Sun. The Examiner stated essentially that the combined teachings of Tsuboyama, Hussain and Sun teach or suggest all of the limitations of claims 1 and 2.

Claim 1 claims, *inter alia*, "a display connected to the host, the display displaying an image, wherein said host transfers packetized image data to the display, said display includes a panel control processor for processing the packetized image data and a panel memory for storing processed image data, wherein the processed image data in the panel memory is displayed as the image, wherein the packetized image data comprises a header identifying the panel control processor and a body including the image data."

As stated in the Office Action, Tsuboyama-Hussain does not explicitly detail the packetized image data having a header and body. Neither Tsuboyama nor Hussain teach or suggest a header, much less "a header identifying the panel control processor" as claimed in claim 1.

Sun teaches an MPEG-like compressed signal format having headers (see col. 2, lines 38-40 and col. 4, lines 5-35). Sun does not teach or suggest that "packetized image data comprises a header identifying the panel control processor" as claimed in claim 1 (Emphasis added). Sun's signal format merely comprises headers for indicating a data type (col. 4, lines 3-4) pointers to data (col. 4, lines 5-20) and headers for identifying portions of the data (col. 4,

lines 21-33). None of the headers of Sun identify a processor. Nowhere does Sun teach or suggest a header identifying a processor, essentially as claimed in claim 1. Therefore, Sun fails to cure the deficiencies of Tsuboyama and Hussain.

The combined teachings of Tsuboyama, Hussain and Sun fail to teach or suggest, “packetized image data comprises a header identifying the panel control processor” as claimed in claim 1.

Claim 2 depends from claim 1. Claim 2 is believed to be allowable for at least the reasons given for claim 1. The Examiner’s reconsideration of the rejection is respectfully requested.

Claim 3 has been rejected under 35 U.S.C. 103(a) as being unpatentable over Tsuboyama in view of Hussain, and further in view of Sun as applied to claim 1, and further in view of U.S. Patent No. 6,611,260 to Greenberg et al. The Examiner stated essentially that the combined teachings of Tsuboyama, Hussain, Sun and Greenberg teach or suggest all the limitations of claim 3.

Claim 3 depends from claim 1. Claim 3 is believed to be allowable for at least the reasons given from claim 1. The Examiner’s reconsideration of the rejection is respectfully requested.

Claim 4 has been rejected under 35 U.S.C. 103(a) as being unpatentable over Tsuboyama in view of Hussain, and further in view of Sun as applied to claim 1, and further in view of U.S. Patent No. 6,097,364 to Miyamoto et al. The Examiner stated essentially that the combined teachings of Tsuboyama, Hussain, Sun and Miyamoto teach or suggest all the limitations of claim 4.

Claim 4 depends from claim 1. Claim 4 is believed to be allowable for at least the reasons given from claim 1.

New claims 29-32 have been added. At least claim 29 is believed to be allowable.

Claim 29 claims, *inter alia*, "a plurality of panel control processors, coupled to said image data receiving means, for processing said image data received from said image data receiving means and displaying a processed image on said panel, wherein said image data comprises a header identifying a first panel control processor from among the plurality of panel control processors."

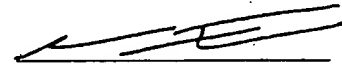
The prior art of record is not believed to teach or suggest, "a header identifying a first panel control processor from among the plurality of panel control processors" as claimed in claim 29. Therefore, claim 29 is believed to be in condition for allowance.

Claims 30-32 depend from claim 29. The dependent claims are believed to be allowable for at least the reasons given for claim 29.

For the forgoing reasons, the application, including claims 1-4, 21 and 29-32, is believed to be in condition for allowance. Early and favorable reconsideration of the case is respectfully requested.

Respectfully submitted,

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